

Xedge MSPx Version 7.2.3

System Software Version 7.2.3 Patch 4a Cumulative Release Notes

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Documentation

Revision History: GDC P/N 032R901-V72304

Issue	Date	Release Description
1	July 2010	Engineering Release of Xedge Version 7.2.3 Patch 4
2	October 2010	Release of Software Version 7.2.3 Patch 4a

Related Publications

Description	Part Number
Xedge Quick Reference (all systems)	032R299-VREV
Xedge Chassis Installation Guide (all models)	032R410-000
Xedge 6000 Switch Technical Reference	032R310-VREV
Xedge 6000 Switch Hardware Installation Manual (for Ver 6.2 Slot Controllers/LIMs)	032R440-V620
Xedge 6000 Switch Configuration Guide (for Ver 6.2 Slot Controllers/LIMs)	032R400-V620
Xedge 6000 Switch Diagnostics Guide ((for Ver 6.2 Slot Controllers/LIMs)	032R500-V620
Xedge 6000 Switch Software Ver 6.2 Release Notes	032R901-V62X
Xedge 6000 Switch Software Ver 6.2 Cumulative Patch Notes	032R901-V62XXX
Xedge MSPx Switch Ver 7.2.2 Config & Diag Guide (for Ver 7.x Slot Controllers/LIMs)	032R401-VREV
Xedge MSPx Software Ver 7.2.3 Release Notes	032R901-V723
ProSphere NMS Version 5.X User Manual	032R610-V5XX
ProSphere NMS Version 5.X Installation and Release Notes	032R906-V5XX
ProSphere NMS Version 4.4.0 Installation and Release Notes (with Managed Video Scheduler)	032R906-V440

-**REV** is the hardware revision (-**000**, -**001**, etc.)

-**VREV** is the most current software version (-**V5X**, **V620**, **V723**, etc.)

In addition to publications listed above, always read any Patch Notes and Addenda supplied with your products.

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Section 1 - Xedge System 7.2.3 Cumulative Patch Notes

0.1 Operational Guidelines for all Upgrades

- Each version patch described in this document builds on the Xedge System Software Version 7.2.3. When upgrading software on Xedge devices, it is important to use files provided on the Release CD. Within the tar files on the CD are ISG2, PCX, PCX2 or PCL kernel files. When a slot controller is loaded with new software and restarted, kernel files are extracted from the **startup_XXX.tz** files, so the **startup_XXX.tz** file size will change significantly after reboot. Do not load the **startup_XXX.tz** file onto other ISG2, PCX, PCX2 or PCL controllers after a reboot, since this file no longer contains the kernel files. If the reduced sized file is used to upgrade a controller, the controller will not receive any kernel updates.
- As of Xedge System Software Version 7.2.2, an md5 checksum file is included with the build. This allows you to verify a file with the following command on a Linux platform:

```
md5sum -c 723v4.md5
```

1.0 Patch 4a Overview (October 2010)

1.1 File Names/Sizes

File Name	Size (bytes)
startup_isg.tz	3195322
startup_isg.tz_pnni	3506380
startup_pcl.tz	3690720
startup_pcx.tz	8373227
startup_pcx.tz_pnni	8682244
startup_pcx2.tz	6874143
startup_pcx2.tz_pnni	7181918

1.2 Features Added / Limitations Resolved

For PCX, PCX2 only:

- Initial ASIO support
- (MR10260) Fixed PCX restart caused by changing admin status of Dry Martini tunnel.
- (MR10267, MR10268) Enhanced error codes and status at physical Ethernet interfaces.
- Initial BGP support
- Fixed various MOLN issues on phe and lsp interfaces.
- Added statistics for ProSphere support.
- Added new MIBs support.
- Added support for Dry Martini over CTag

- (MR10268) Fixes for Eth and LSP terminations.
- Updated defaults for VLAN configurations.
- (MR10259) Fixed problem with spvc getting max sap connection and not marking route as failed.
- (MR10281) Fixed problem with virtual saps not getting activated when out of service card became in service.
- Fixed random kernel crash found during load testing local basecard router connections.
- Support for PVID on VLAN local basecard router connections.
- (MR10289) Fixed crash if stale handle, Don't allow VLAN 0 when configured for VLAN
- MR10270 Fixed Eth link alarm.
- MR10320 Fixed BW bump issue on PCX, which caused watchdog timeout.
- Fixed latency for local delivery IP routing.
- Fixed DP crash caused by invalid info.

For PCX2 only:

- Added support for MPLS and DM PW. Can be used as substitute for PCX.
- Initial Support for IP PW.
- Increased local delivery rate for phe and lsp interfaces.
- Support for pvid uni transport over pw over mpls
- Added Tx Frames Discard to the evcVlanStatusTable
- Added a new byte shaper script for frame tspecs.
- Use queue rate scaling to do shaping for low rates

For all packet controllers (PCX, PCX2, PCE, PCL, ISG2, etc.)

- Change max slots for health check to actual number of slots in shelf. Originally all 16 slots were checked regardless of shelf.
- Fixed ATM port Bandwidth hangup.

Note: For operational guidelines, refer to [Section 0.1 Operational Guidelines for all Upgrades](#).

2.0 Patch 3 Overview (April 2010)

2.1 File Names/Sizes

File Name	Size (bytes)
<code>startup_isg.tz</code>	3174977
<code>startup_isg.tz_pnni</code>	3483561
<code>startup_pcl.tz</code>	3669489
<code>startup_pcx.tz</code>	8115540
<code>startup_pcx.tz_pnni</code>	8420443
<code>startup_pcx2.tz</code>	6586020
<code>startup_pcx2.tz_pnni</code>	6891485

2.2 Limitations Resolved

For all packet controllers (PCX, ISG2, PCL, etc.)

- Fixed lockup caused by timeout of the Craft port while at the MIB option screen.
- Fixed SNMP semaphore issues.
- Changed File System updates to more recent releases. This significantly reduces the size of the `startup_xxx.tz` file.
- Stopped removal of log files from /var/log files during update, i.e., XOSLOG.

For PCX, PCX2 only:

- Fixed bug with bandwidth and CAC when using localdel and phy terminations.
- Added IP tunnel support for OSPF configuration in IP Config tables.
- Provided a number of MOLN fixes for PHE interfaces.
- Fixed CAC problems with OAM and low speed interfaces.
- Added timer to check for MOLN timeouts.
- Fixed Data Path FPGA link restrictors, an overlap of PW/Multicast loop ports and LIM port was discovered causing restricted flow for PW traffic.
- Enabled egress priority tagging for LSP connections.

For PCX2 only:

- Fixed problems with Network Processor policer and shaper code.

Note: For operational guidelines, refer to [Section 0.1 Operational Guidelines for all Upgrades](#).

3.0 Patch 2 Overview (February 2010)

3.1 File Names/Sizes

File Name	Size (bytes)
startup_isg.tz	4472881
startup_isg.tz_pnni	4783384
startup_pcl.tz	4965964
startup_pcx.tz	9410190
startup_pcx.tz_pnni	9712936
startup_pcx2.tz	7855942
startup_pcx2.tz_pnni	8164426

3.2 Limitations Resolved

For all packet controllers (PCX, ISG2, PCL, etc.)

- Fixed RIP multicast membership issues which caused MOLN to fail updates.
- (MR10188) Fixed SPVC point-multipoint failure in intra-slot spacial multicast
- (MR10189) Fixed multicast release problem where releasing second level root marked SVC entry invalid
- (MR10203) Fixed communication problem between PNNI router and PNNI interfaces.
- (MR10206) Fixed problem with PNNI sap (0/18) which did not come up after reboot.
- MR10211) Fixed SPVC flow control which failed to retry spvcs in the table.

For ISG2 only:

- Fixed problems with PVC establishment after slot0 ISG2 redundant switchover.
- (MR10207) Fixed problem in PNNI route table caused by slot0 ISG2 redundant switchover. PNNI Route table loses remote reachable addresses.
- (MR10208) Fixed problem in PNNI route table caused by slot0 ISG2 redundant switchover. PNNI Route table loses local addresses.

For PCX, PCX2 and PCL only:

- Fixed problems with LIM reboot caused when there are active file transfers or any tasks with high CPU usage.

For PCX and PCX2 only

- Fixed handling of messages in ospfd and rsvpd, caused when they were moved to M,94,22.
- Fixed DataPath FPGA lockup during controller restart.

For PCX2 only:

- (MR10202) Fixes shaper memory usage problems when large volume of connections are being shaped simultaneously.
- (MR10130) Fixes problem where the Network Processor data path crashes when VC Status is being viewed.

Note: For operational guidelines, refer to [Section 0.1 Operational Guidelines for all Upgrades](#).

4.0 Patch 1 Definitions (December 2009)

4.1 File Names/Sizes

File Name	Size (bytes)
<code>startup_isg.tz</code>	4466982
<code>startup_isg.tz_pnni</code>	4775852
<code>startup_pcl.tz</code>	4964827
<code>startup_pcx.tz</code>	9407895
<code>startup_pcx.tz_pnni</code>	9710350
<code>startup_pcx2.tz</code>	7853453
<code>startup_pcx2.tz_pnni</code>	8162565
<code>oc12.cod</code>	229224

4.2 Limitations Resolved

For PCX, PCX2 only:

- Fixed DataPath FPGA causing bit errors to NP.
- Added OSPF MIB support (new MIB file `ipCfg.mib`)
- Added debug for GenPC and NexPC

For PCX and PCL only:

- Fixed CntlPath FPGA causing bit errors to basecard Switch Fabric interface.

For OCN LIM only:

- New `oc12.cod`, version 1.05 fixes APS issues.

Note: For operational guidelines, refer to [Section 0.1 Operational Guidelines for all Upgrades](#).
